## IN THE CLAIMS:

Please amend the claims as follows:

 (Currently Amended) A weighting circuit for a receiver (1) which is provided for receiving a multicarrier signal comprising carrier signals, where the weighting circuit (18) weights the carrier signals such that the spurious signal energy is of equal magnitude for all weighted carrier signals.

A weighting circuit for a receiver which is provided for receiving a multicarrier signal comprising carrier signals,

where the weighting circuit weights the carrier signals such that the spurious signal energy is of equal magnitude for all weighted carrier signals,

where the weighting circuit has a memory, which stores a plurality of weighting coefficient sets, and a selector which selects one of the weighting coefficient sets stored in the memory on the basis of an expected spurious signal energy in the received signal,

where the expected spurious signal energy is calculated by an estimation unit using cross correlation between the received signal and a spurious signal to be expected which has been phase-shifted through 90°.

- 2. (Currently Amended) The weighting-circuit as claimed in claim 1, characterized in that the weighting circuit (18) has at least one multiplier (7-i) which multiplies an associated carrier signal by a stored weighting coefficient (gi) from the selected weighting coefficient set.
  - The weighting circuit as claimed in claim 1, wherein the weighting circuit has at least one multiplier which multiplies an associated carrier signal by a stored weighting coefficient from the selected weighting coefficient set.
- 3. (Currently Amended) The weighting circuit as claimed in claim 2, characterized in that the weighting circuit (18) has a memory which

stores a plurality of weighting coefficient sets (G<sub>i</sub>) which each comprise a plurality of weighting coefficients (g<sub>i</sub>).

The weighting circuit as claimed in claim 1, wherein the memory can be programmed via an interface.

4. (Currently Amended) The weighting circuit as claimed in claim 3, characterized in that the memory (9) can be programmed via an interface.

The weighting circuit as claimed in claim 3, wherein the multicarrier signal is broken down into the carrier signals by a computation circuit.

- 5. (Currently Amended) The weighting circuit as claimed in claim 3, characterized in that the weighting circuit (18) has a selector (15) which selects one of the weighting coefficient sets (G<sub>i</sub>) stored in the memory. The weighting circuit as claimed in claim 4, wherein the computation circuit is a Fast Fourier Transformation circuit.
- 6. (Currently Amended) The weighting circuit as claimed in claim 5, characterized in that the selector (15) selects a weighting coefficient set (G<sub>i</sub>) on the basis of an expected spurious signal energy. The weighting circuit as claimed in claim 5, wherein the carrier signals broken down by the computation circuit are buffer-stored in a buffer store.
- 7. (Currently Amended) The weighting circuit as claimed in claim 6, characterized in that the selector (15) selects a weighting coefficient set (G<sub>i</sub>) on the basis of an averaged frequency offset between the maximum of the spurious signal spectrum and the next closest carrier signal of the multicarrier signal.

The weighting circuit as claimed in claim 1, wherein the expected spurious signal energy can be set externally.

- 8. (Deleted) The weighting-circuit as claimed in claim 7, characterized in that the multicarrier signal is broken down into the carrier signals by a computation circuit (5).
- 9. (Deleted) The weighting circuit as claimed in claim 8, characterized in that the computation circuit (5) is a Fast Fourier Transformation circuit.
- 10. (Deleted) The weighting circuit as claimed in claim 9, characterized in that the carrier signals broken down by the computation circuit (5) are buffer-stored in a buffer store (24).
- 11. (Deleted) The weighting circuit as claimed in claim 6, characterized in that the expected spurious signal energy can be set externally.
- 12. (Deleted) The weighting circuit as claimed in claim 6, characterized in that the expected spurious signal energy is calculated by an estimation unit (25) on the basis of the received multicarrier signal.